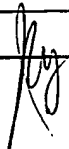
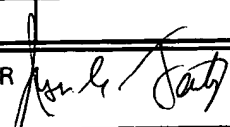




INFORMATION DISCLOSURE CITATION PTO-1449		Customer Number 26615	ATTORNEY'S DKT No. H1484		APPLICATION No. 10/614,001		
			APPLICANT(S) Shibly S. Ahmed et al.				
			FILING DATE July 8, 2003		GROUP 2811		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	Co-pending U.S. Application Serial No. 10/674,520 filed October 2, 2003 entitled: "Damascene FinFET Gate With Selective Metal Interdiffusion," 16 page specification, 19 sheets of drawings.						
EXAMINER 				DATE CONSIDERED 9/19/04			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

SHEET 1 OF

**INFORMATION
DISCLOSURE
CITATION**
PTO-1449

Customer Number
26615

ATTORNEY'S DKT NO.
H1484

APPLICATION No.
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APPLICANT(S)
Shibly S. Ahmed et al.

FILING DATE
July 8, 2003

GROUP
2815


U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

 Copy of co-pending U.S. Patent Application Serial No. 10/857,931, filed June 2, 2004, titled: "Selective Channel Implantation for Forming Semiconductor Devices with Different Threshold Values," Haihong Wang et al., 21page specification, 20 sheets of drawings.

EXAMINER

DATE CONSIDERED

9/17/4